



Form PTO 449 U.S. Department of Commerce Patent and Trademark Office				Atty. Docket No. IMPJ-0003D1		Serial No.: 10/661,037		
Information Disclosure Statement by Applicant				Applicant: Christopher J. Diorio et al.				
(Use several sheets if necessary)				Filed: September 12, 2003 Group: <del>Unassigned</del> 2822				
<b>U.S. Patent Documents</b>								
Init.		Document No.	Date	Name	Class	Subclass	Filing Date	
<i>AK</i>	A	4,783,783	11/8/1988	Nagai et al.			7/24/1986	
	B	4,935,702	6/19/1990	Mead et al.			12/9/1988	
	C	5,933,039	8/3/1999	Hui et al.			3/25/1997	
	D	5,939,945	8/17/1999	Thewes et al.			7/25/1997	
	E	6,134,182	10/17/2000	Pilo et al.			10/19/1999	
<i>AK</i>	F	6,320,788	11/20/2001	Sansbury et al.			9/11/2000	
<b>Foreign Documents</b>								
Init.		Document No.	Date	Country	Class	Subclass	Translation	
<i>AK</i>	G	0 298 618	1/11/1989	EP			Yes	No
							X	
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>								
<i>AK</i>	H	L. Richard Carley, "Trimming Analog Circuits Using Floating-Gate Analog MOS Memory", IEEE Journal of Solid-State Circuits 24, No. 6, December 1989, pp. 1569-1575.						
<i>AK</i>	I	Partial International Search for International Application No. PCT/US03/31792, date mailed April 2, 2004.						
Examiner <i>AK</i>					Date Considered <i>8-20-04</i>			
Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.								